

PREVIEW QUESTION BANK

Module Name : cec24-cs09 Digital Electronics and Microprocessor-ENG
Exam Date : 18-May-2024 Batch : 09:00-12:00

Sr. No.	Client Question ID	Question Body and Alternatives	Marks	Negative Marks
Objective Question				
1	14481001	<p>Which number system is used in Microprocessor?</p> <ol style="list-style-type: none">1. Binary2. Octal3. Decimal4. Hexadecimal <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
2	14481002	<p>What is the memory capacity of the processor having 16 address lines?</p> <ol style="list-style-type: none">1. 32KB2. 64KB3. 1MB4. 2MB <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
3	14481003	<p>Which one converts assembly language into machine language?</p> <ol style="list-style-type: none">1. Assembler2. Interpreter3. Linker4. Translator <p>A1 : 1</p>	2.0	0.00

		A2 : 2		
		A3 : 3		
		A4 : 4		

Objective Question

4	14481004	<p>Which signal is used to demultiplex the address and data lines?</p> <ol style="list-style-type: none"> 1. ALE 2. HOLD 3. TRAP 4. SOD <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
---	----------	---	-----	------

Objective Question

5	14481005	<p>When bit of flag register is set, then identify which flag is affected?</p> <ol style="list-style-type: none"> 1. Parity flag 2. Sign flag 3. Carry flag 4. Zero flag <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
---	----------	--	-----	------

Objective Question

6	14481006	<p>Which is the lowest priority interrupt?</p> <ol style="list-style-type: none"> 1. RST 6.5 2. TRAP 3. INTR 4. RST 7.5 <p>A1 : 1</p> <p>A2 : 2</p>	2.0	0.00
---	----------	---	-----	------

		A3 : 3		
		A4 : 4		

Objective Question

7	14481007	<p>What is the size of program counter?</p> <ol style="list-style-type: none"> 1. 4 bits 2. 8 bits 3. 16 bits 4. 32 bits <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
---	----------	--	-----	------

Objective Question

8	14481008	<p>Which one is used to demultiplex address and data lines?</p> <ol style="list-style-type: none"> 1. Buffer 2. Latch 3. Decoder 4. Demux <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
---	----------	---	-----	------

Objective Question

9	14481009	<p>When borrow is generated during hexadecimal subtraction which flag is affected?</p> <ol style="list-style-type: none"> 1. Zero flag 2. Borrow flag 3. Carry flag 4. Auxillary carry flag <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p>	2.0	0.00
---	----------	---	-----	------

		A4 : 4		
Objective Question				
10	14481010	<p>What is the principle of stack pointer?</p> <ol style="list-style-type: none"> 1. FIFO 2. LIFO 3. FILO 4. LILO <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
11	14481011	<p>Convert $(125)_8$ into decimal</p> <ol style="list-style-type: none"> 1. $(58)_{10}$ 2. $(45)_{10}$ 3. $(55)_{10}$ 4. $(85)_{10}$ <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
12	14481012	<p>Convert $(175.13)_{10}$ into binary number</p> <ol style="list-style-type: none"> 1. $(10101100.010)_2$ 2. $(10101111.001)_2$ 3. $(10110111.001)_2$ 4. $(11011111.001)_2$ <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00

Objective Question				
13	14481013	<p>For the given numbers 11011-10010, do the subtraction using 1's complement</p> <p>1. 10001 2. 01010 3. 01001 4. 10010</p> <p>A1 : 1 A2 : 2 A3 : 3 A4 : 4</p>	2.0	0.00
Objective Question				
14	14481014	<p>Add these two binary numbers 0101+1110</p> <p>1. 10011 2. 10110 3. 01011 4. 10010</p> <p>A1 : 1 A2 : 2 A3 : 3 A4 : 4</p>	2.0	0.00
Objective Question				
15	14481015	<p>Convert $(78B)_{16}$ into octal</p> <p>1. $(6313)_8$ 2. $(3613)_8$ 3. $(3316)_8$ 4. $(3611)_8$</p> <p>A1 : 1 A2 : 2 A3 : 3 A4 : 4</p>	2.0	0.00
Objective Question				
16	14481016		2.0	0.00

		<p>The HL register pair is called as</p> <ol style="list-style-type: none"> 1. A register 2. B register 3. M register 4. C register <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>		
Objective Question				
17	14481017	<p>In which T state ALE is high?</p> <ol style="list-style-type: none"> 1. T1 2. T2 3. T3 4. T4 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
18	14481018	<p>LDA 3000H, Which operation is performed?</p> <ol style="list-style-type: none"> 1. MEMR 2. MEMW 3. IOR 4. IOW <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
19	14481019		2.0	0.00

		<p>OUT 27H, Which operation is performed?</p> <ol style="list-style-type: none"> 1. MEMR 2. MEMW 3. IOR 4. IOW <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>		
--	--	--	--	--

Objective Question

20	14481020	<p>What is the addressing mode of given instruction LDAX B?</p> <ol style="list-style-type: none"> 1. Immediate addressing mode 2. Register addressing mode 3. Direct addressing mode 4. Register indirect addressing mode <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

21	14481021	<p>JMP 4526H, Identify the type of addressing mode?</p> <ol style="list-style-type: none"> 1. Immediate addressing mode 2. Register addressing mode 3. Direct addressing mode 4. Register indirect addressing mode <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

22	14481022		2.0	0.00
----	----------	--	-----	------

		<p>Which instruction is used to complement the carry flag?</p> <ol style="list-style-type: none"> 1. STC 2. CLC 3. CLR 4. CMC <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>		
Objective Question				
23	14481023	<p>Which instruction is used to terminate the sub-program?</p> <ol style="list-style-type: none"> 1. RET 2. HLT 3. STOP 4. IRET <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
24	14481024	<p>Which interrupt is vector interrupt?</p> <ol style="list-style-type: none"> 1. TRAP 2. RST 6.5 3. RST 7.5 4. INTR <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
25	14481025		2.0	0.00

		<p>How many T states are required to execute LXI H, 2000H Instruction?</p> <ol style="list-style-type: none">1. 42. 73. 104. 14 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>		
Objective Question				
26	14481026	<p>How many machine cycles are required to execute the instruction SHLD 5000H?</p> <ol style="list-style-type: none">1. 22. 33. 44. 5 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
27	14481027	<p>Find the value of accumulator after execution of the following instructions:</p> <p>MVI A,55H</p> <p>SUI 23H</p> <p>HLT</p> <ol style="list-style-type: none">1. 06H2. 03H3. 32H4. 23H <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00

Objective Question

28	14481028	MVI A,27H ANI 32H RRC RRC STA 3000H What is the content of memory location 3000H after execution? 1. 11H 2. 88H 3. 44H 4. 55H A1 : 1 A2 : 2 A3 : 3 A4 : 4	2.0	0.00
----	----------	--	-----	------

Objective Question

29	14481029	Which signal is used for serial communication in 8085? 1. HOLD 2. HLDA 3. INTR 4. SID A1 : 1 A2 : 2 A3 : 3 A4 : 4	2.0	0.00
----	----------	---	-----	------

Objective Question

30	14481030	If $A > B$, which of the following is true? 1. Carry flag set 2. Carry flag reset 3. Auxillary carry set 4. Auxillary carry reset	2.0	0.00
----	----------	--	-----	------

		A1 : 1		
		A2 : 2		
		A3 : 3		
		A4 : 4		

Objective Question

31	14481031	<p>Identify the unconditional branching statement?</p> <ol style="list-style-type: none"> 1. JNC 4500H 2. JNZ 2000H 3. JMP 3000H 4. JC 1500H <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

32	14481032	<p>Which instruction is used to read the data from the stack pointer?</p> <ol style="list-style-type: none"> 1. PUSH 2. POP 3. STA 2500H 4. MOV A,M <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

33	14481033	<p>Which flag is affected during the execution of the interrupt?</p> <ol style="list-style-type: none"> 1. Zero flag 2. Carry flag 3. Sign flag 4. None <p>A1 : 1</p>	2.0	0.00
----	----------	---	-----	------

A2 : 2

A3 : 3

A4 : 4

Objective Question

34	14481034	<p>Before switching into sub-program, microprocessor pushes the current address into _____</p> <ol style="list-style-type: none"> 1. HL register 2. Accumulator 3. Program Counter 4. Stack pointer <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

35	14481035	<p>Interrupt can be enabled, but cannot be disabled is called _____</p> <ol style="list-style-type: none"> 1. Maskable Interrupt 2. Non-maskable Interrupt 3. Vector Interrupt 4. Non-vector Interrupt <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

36	14481036	<p>How many pins are available for parallel communication in 8155?</p> <ol style="list-style-type: none"> 1. 11 2. 16 3. 22 4. 18 <p>A1 : 1</p> <p>A2 : 2</p>	2.0	0.00
----	----------	---	-----	------

A3 : 3

A4 : 4

Objective Question

37	14481037	<p>Once octal latch is enabled by ALE, how many output lines are generated?</p> <ol style="list-style-type: none"> 1. 4 2. 6 3. 8 4. 10 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

38	14481038	<p>8255 is mainly used for which communication?</p> <ol style="list-style-type: none"> 1. Serial 2. Parallel 3. Serial & Parallel 4. None <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

39	14481039	<p>Which mode is bi-directional mode in 8255?</p> <ol style="list-style-type: none"> 1. Mode 0 2. Mode 1 3. Mode 2 4. Mode 3 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p>	2.0	0.00
----	----------	--	-----	------

		A4 : 4		
Objective Question				
40	14481040	<p>Group A of 8255 consists of</p> <ol style="list-style-type: none"> 1. Port A and Port C_L 2. Port A and Port C_u 3. Port B and Port C_L 4. Port B and Port C_u <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
41	14481041	<p>When the bit position of control word is 1 in 8255, then it will select</p> <ol style="list-style-type: none"> 1. IO mode 2. BSR mode 3. Only port C 4. Only output mode <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
Objective Question				
42	14481042	<p>Which signal is used to stop sending the data of processor to external device?</p> <ol style="list-style-type: none"> 1. Strobe 2. Acknowledgement 3. Buffer full 4. INTR <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00

Objective Question

43	14481043	<p>Which port is not used in Mode 2 of 8255?</p> <ol style="list-style-type: none">1. Port A2. Port B3. Upper port C4. Lower port C <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

44	14481044	<p>How many handshake signals used in Mode 1 input configuration of 8255?</p> <ol style="list-style-type: none">1. 32. 43. 54. 6 <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

45	14481045	<p>What is the control word for assigning group A as output port and group B as input port in Mode 0?</p> <ol style="list-style-type: none">1. 80H2. 63H3. 83H4. 58H <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

46	14481046	<p>Which instruction is used to write the data into the memory?</p> <ol style="list-style-type: none">1. OUT2. IN3. LDA4. STA <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

47	14481047	<p>Find BSR control word to set PC5</p> <ol style="list-style-type: none">1. 0BH2. B0H3. 0CH4. 0AH <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	---	-----	------

Objective Question

48	14481048	<p>Which is not an output configuration handshake signal in 8255?</p> <ol style="list-style-type: none">1. OBF2. ACK3. STB4. INTR <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------

Objective Question

49	14481049		2.0	0.00
----	----------	--	-----	------

		<p>Which port is set/Reset by BSR control word?</p> <ol style="list-style-type: none">1. Port A2. Port B3. Port C4. Port D <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>		
--	--	---	--	--

Objective Question

50	14481050	<p>Which instruction is used to display the LED which are connected in Port B of Mode 1 configuration in 8255?</p> <ol style="list-style-type: none">1. IN port A2. OUT port A3. IN port B4. OUT port B <p>A1 : 1</p> <p>A2 : 2</p> <p>A3 : 3</p> <p>A4 : 4</p>	2.0	0.00
----	----------	--	-----	------